IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application:

T. ISHII et al

Serial No. 10/388,444 Filed: March 17, 2003

Group Art Unit:

2824

Examiner:

H. Nguyen

For:

NONVOLATILE MEMORY SYSTEM,

SEMICONDUCTOR MEMORY AND WRITING

METHOD

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

October 9, 2003

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 10/388,444.

The Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,

Registration No. 32,846 Attorney for Applicant(s)

MATTINGLY, STANGER & MALUR, P.C. 1800 Diagonal Road, Suite 370 Alexandria, Virginia 22314 Telephone: (703) 684-1120 Facsimile: (703) 684-1157

Date: October 9, 2003

FORM PTO-1449 (REV. 7-80) U.S. DEPARTMENT OF COMMERCI PATENT AND TRADEMARK OFFICE LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)					H-626-11 APPLICANT T. ISHII et al FILING DATE GROUP					
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